A Reachability Index for Recursive Label-Concatenated Graph Queries

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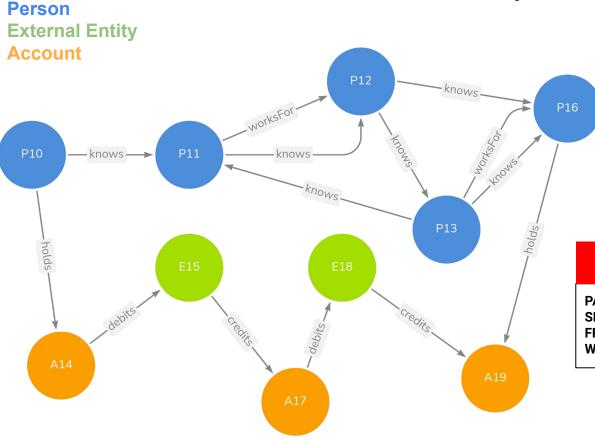




Oracle Labs PGX



An interleaved social and professional network



Money laundering analysis:

Do accounts 14 and 19 have the repeated outside-inside money transferring pattern?

PGQL

PATH out_in AS () -[:debits-]> () -[:credits]-> () SELECT * FROM MATH (s) -/:out_in+/-> (t) WHERE ID(s) = 14 AND ID(t) = 19

RLC (recursive label-concatenated) queries

- Path constraints
 - A concatenation of edge labels under the Kleene plus, i.e., $(I_1, ..., I_k)^+$
- RLC query (s, t, L⁺), L = (I₁, ..., I_k), checks
 - whether there is a path from vertex s to vertex t, and
 - whether the edge label sequence of the path matches the path constraint L⁺
- No constraint on path length
 - Paths selected by RLC query can have an arbitrary length
- Boolean query
 - Returns either True or False

RLC queries in mainstream graph processing systems

RLC queries cannot be expressed in

- Cypher of Neo4j (v4.3)
- GSQL of TigerGraph (v3.3)

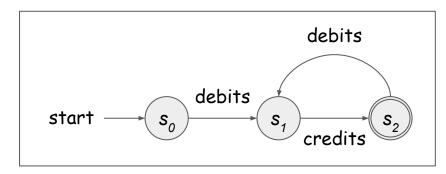
RLC queries **can** be expressed in

- SPARQL 1.1, supported by Virtuoso, Apache Jena, etc
- PGQL of Oracle PGX
- Gremlin, supported by TinkerPop-Enabled Graph System, e.g., Amazon Neptune

RLC queries can be expressed in **GQL** or **SQL/PGQ** [Deu22]

RLC query processing

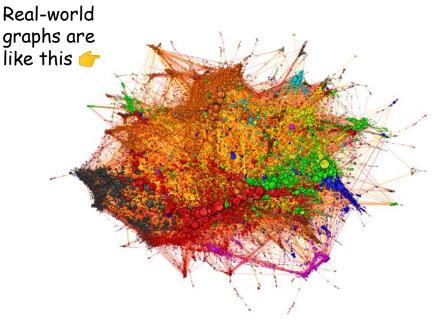
- Path semantics
 - Simple paths: non-repeated vertices or edges
 - **Arbitrary paths**: vertices or edges can repeat
- Building an FA (Finite Automata) based on the path constraint
- Query processing: online traversal guided by an FA, e.g., BFS guided by an FA



FA of (debits, credits)+

RLC query processing

- Problem
 - real-world graphs are large
- RLC queries often timed out [Bon19]
- Building an index for online RLC queries
 - Fast query processing
 - Efficient index computation and storage



Music recommendation graph: http://sixdegrees.hu/last.fm/interactive_map.html

Related works: reachability indexes

Plain reachability indexes

- **Queries**: checking the existence of a path only
- Indexes: Tree Cover, 2-Hop labeling, Dual labeling, TFL, TOL, GRAIL, BFL, etc

Infeasible for RLC queries due to missing the support for evaluating path constraints Label-constrained reachability indexes

- Queries: checking the existence of a path and whether the edge-label set of the path is a subset of a given edge-label set
- Indexes: Landmark Index, P2H, etc

Infeasible for RLC queries due to different path constraints, i.e., set vs sequence

RLC Index

Index structure

Vertex v	Lout(v)	Lin(v)

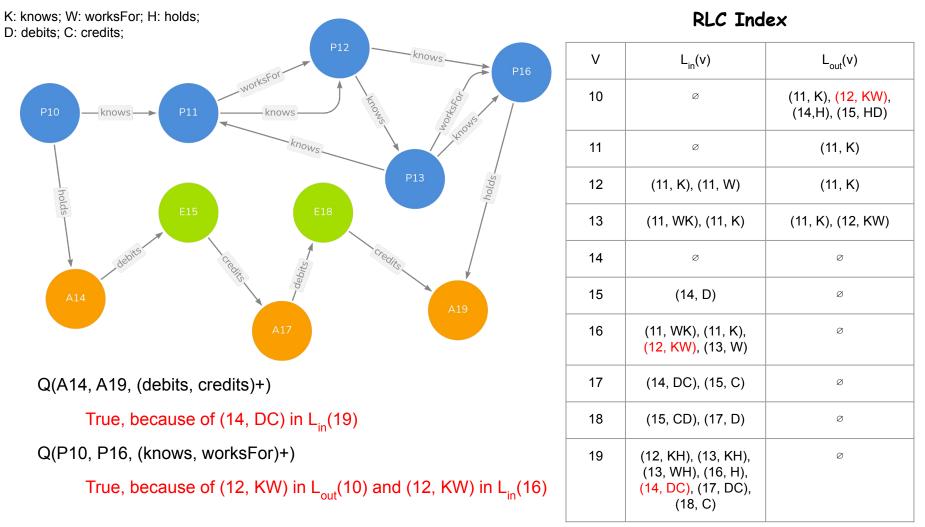
Lout(v): recording reachability information from v

Lin(v): recording reachability information to v

Schema of Lout(v) or Lin(v): (u, mr), where mr is a succinct path label sequence, defined later on

E.g., if (u, mr) is in Lout(v), then there is a path from v to u with the succinct path label sequence is mr

Alg	Algorithm 1: Query Algorithm.		
1 pr	ocedure Query (s, t, L^+)		
2	if $\exists (t,L) \in \mathcal{L}_{out}(s)$ or $\exists (s,L) \in \mathcal{L}_{in}(t)$ then		
3	return true;		
4	for $(I', I'') \in mergeJoin(\mathcal{L}_{out}(s), \mathcal{L}_{in}(t))$ do		
5	if $I'.mr = L$ and $I''.mr = L$ then		
6	return true;		
7 return <i>false</i> ;			



Challenge

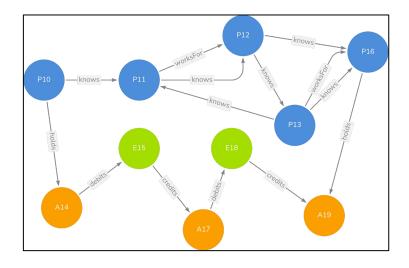
how to build RLC Index

Challenge C1: infinite edge-label sequences

Challenge C2: efficient indexing algorithm

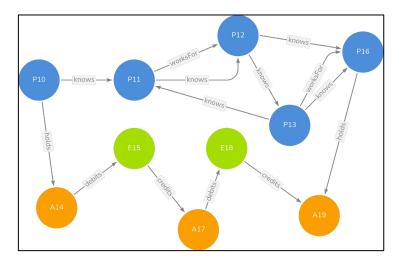
Indexing edge-label sequence

- Edge-label sequence is necessary
- Example
 - The sequence (debits, credits, debits, credits) is necessary for query (A14, A19, (debits, credits)+)
- Succinct representation: MR (minimum repeat)
- Example
 - MR((debits, credits, debits, credits)) = (debits, credits)



Indexing edge-label sequence

- Question: how many MRs from P11 to P13
 - Infinite due to the cycle with P11, P12, and P13
- Observation of real-world RLC queries
 - The length of recursive concatenation is bounded, i.e., $(I_1, ..., I_k)^+$, where k is bounded
- Question: given $k \le 2$, for P11 and P13
 - how many MRs of length up to 2
 - how to compute all the MRs



KBS (kernel-based search)

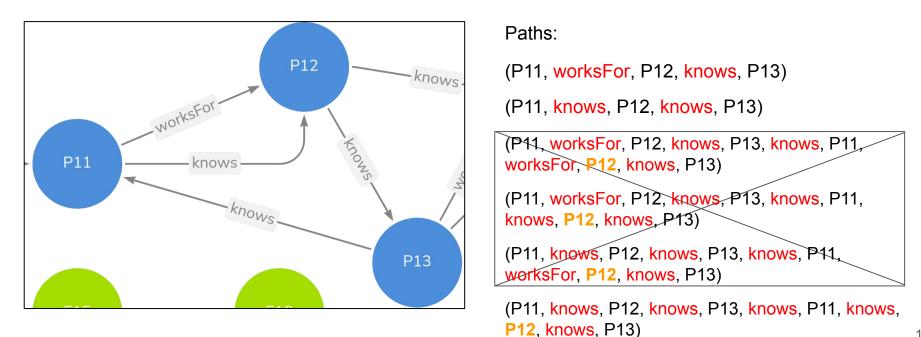
- Intuition: generating **kernels** on the fly guiding the computation of MRs using kernels
- Example of kernel
 - Label sequence: $(I_1, I_2, I_1, I_2, I_1)$
 - Kernel: (I_1, I_2)
- KBS: two-phase search
 - Kernel search
 - Kernel BFS

Kernel

Definition 3: If a label sequence L can be represented as $L = (L')^h \circ L''$, where $h \ge 2$, $L' \ne \epsilon$ and MR(L') = L', and L'' is ϵ or a proper prefix of L', then L has the kernel L' and the tail L''.

Running example: KBS

Given $k \le 2$, computing all the MRs from P11 to P13



Foundations of KBS

Sufficient and necessary conditions

Our conditions are correct

The results are complete

Theorem 1: Given a path p from u to v and a positive integer k, p has a non-empty k-MR if and only if one of the following conditions is satisfied,

- Case 1: $|p| \leq k$. $MR(\Lambda(p))$ is the k-MR of p;
- Case 2: $k < |p| \le 2k$. If $|MR(\Lambda(p))| \le k$, $MR(\Lambda(p))$ is the k-MR of p;
- Case 3: |p| > 2k. Let x be the intermediate vertex on p, s.t. |p(u,x)| = 2k. If $\Lambda(p(u,x))$ has a kernel L' and a tail L'', and $MR(L'' \circ \Lambda(p(x,v))) = L'$, then L' is the k-MR of p.

The proof is included in the full version on <u>arXiv</u>

Lazy KBS vs eager KBS

Lazy KBS: computing kernels when the length of edge label sequence is **2k**

Getting **valid** kernels first, and then using the kernel to guide the search

Eager KBS: computing kernels when the length of edge label sequence is **k**

Getting **valid** and **invalid** kernels first, and then using both of them to guide the search

Eager KBS is more efficient than lazy KBS, because the depth of its traversal is shorter

Eager KBS is also correct, because searches guided by invalid kernels will not reach target

Challenge

how to have RLC index

Challenge C1: infinite edge-label sequence

Challenge C2: efficient indexing algorithm

Indexing algorithm

- Performing backward and forward KBS from each vertex
- Each KBS contains two phases
 - kernel search
 - kernel BFS
- During kernel search
 - computing and inserting MRs for each traversal step
- During kernel BFS
 - inserting MRs only if they are same as kernels

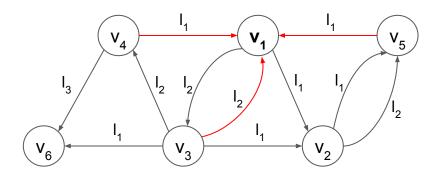
	gorithm 2: Indexing Algorithm.
-	rocedure kernelBasedSearch(v, k)
2	for $(L, vSet) \in backwardKernelSearch(v, k)$ do
3	backwardKernelBFS($v, vSet, L$);
4	for $(L, vSet) \in forwardKernelSearch(v, k)$ do
5	forwardKernelBFS($v, vSet, L$);
6 p	rocedure backwardKernelSearch(v, k)
7	$q \leftarrow$ an empty queue of (vertex, label sequence);
8	q.enqueue (v, ϵ) ;
9	$map \leftarrow a map$ of (kernel candidates, vertex set);
10	while q is not empty do
11	$ (x, seq) \leftarrow q. dequeue();$
12	for incoming edge $e(y, x)$ to x do
13	$ $ seq' $\leftarrow \lambda(e(y, x)) \circ seq; L \leftarrow MR(seq');$
14	insert $(y, v, L);$
15	map.get(L).add(x);
16	if $ seq' < k$ then
17	q.enqueue(y, seq');
18	return map;
0 -	
÷	rocedure insert (s, t, L) if $aid(t) > aid(s)$ or $Query(s, t, L^+)$ then
20	
21	return <i>false</i> ;
22	else
23	add (t, L) into $\mathcal{L}_{out}(s)$;
24	return true;
•	rocedure backwardKernelBFS ($v, vSet, L$)
26	$q \leftarrow$ an empty queue of (vertex, integer);
27	for $x \in vSet$ do
28	mark x as visited by state 1, q.enqueue($x, L $);
29	while q is not empty do
20	$(x,i) \leftarrow q.$ dequeue(), $i \leftarrow i-1;$
30	if $i = 0$ then $i = L $;
31	label $l \leftarrow L.get(i);$
31 32	
31 32 33	label $l \leftarrow L.get(i);$
31 32 33	label $l \leftarrow L.get(i)$; for incoming edge $e(y, x)$ to x do
31 32 33 34 35	label $l \leftarrow L.get(i)$; for incoming edge $e(y, x)$ to x do if $l \neq \lambda(e(y, x))$ or y was visited by state i then continue;
31 32 33 34 35 36	$ \begin{array}{l} \text{label } l \leftarrow L.get(i); \\ \text{for incoming edge } e(y,x) \text{ to } x \text{ do} \\ & \text{if } l \neq \lambda(e(y,x)) \text{ or } y \text{ was visited by state } i \text{ then} \\ & \ \ \ \ \ \ \ \ \ \ \ \ \$
31 32 33 34 35	label $l \leftarrow L.get(i)$; for incoming edge $e(y, x)$ to x do if $l \neq \lambda(e(y, x))$ or y was visited by state i then continue;

Backward KBS from v₁ Kernel search Kernel BFS

I₁ I₁ V₄ **V**₁ V₅ I_3 1₂ 1₁ **'**2/ 12 **1**2 1₁ 1₁ V₃ V₆ V_2

V	Lout(v)	Lin(v)
V ₁		
V ₂		
V ₃		
V ₄		
V ₅		
V ₆		

Backward KBS from v₁ ➡ Kernel search Kernel BFS

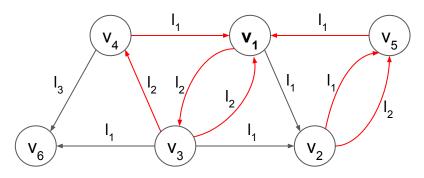


V	Lout(v)	Lin(v)
V ₁		
V ₂		
V ₃	(v ₁ , l ₂)	
V ₄	(v ₁ , l ₁)	
V_5	(v ₁ , l ₁)	
v ₆		

Backward KBS from v₁ Kernel search

Kernel BFS

 \square



Kernel search terminates

Three kernels have been generated

- 1. (l_1) with a set of frontier vertices $\{v_4, v_5, v_2\}$
- 2. (l_2) with a set of frontier vertices $\{v_3, v_1\}$
- 3. (l_2, l_1) with a set of frontier vertices $\{v_3, v_2\}$

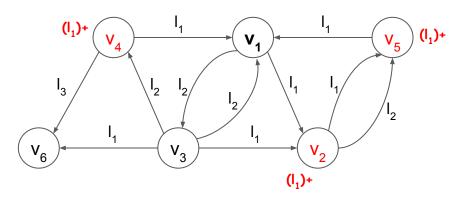
V	Lout(v)	Lin(v)
V ₁	(v ₁ , l ₂)	
V ₂	$(v_1, l_1), (v_1, (l_2, l_1))$	
V ₃	$(v_1, l_2), (v_1, (l_2, l_1))$	
V ₄	(v ₁ , l ₁)	
V ₅	(v ₁ , l ₁)	
V ₆		

Backward KBS from v₁

Kernel search

→ Kernel BFS

The first kernel (I1)+ with a set of frontier vertices {v4, v5, v2}



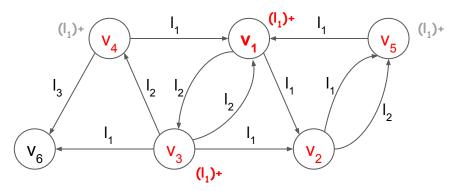
V	Lout(v)	Lin(v)
V ₁	(v ₁ , l ₂)	
V ₂	$(v_1^{}, l_1^{}), (v_1^{}, (l_2^{}, l_1^{}))$	
V ₃	$(v_1^{}, l_2^{}), (v_1^{}, (l_2^{}, l_1^{}))$	
V ₄	(v ₁ , l ₁)	
V ₅	(v ₁ , l ₁)	
v ₆		

Backward KBS from v_1

Kernel search

⇒ Kernel BFS

The first kernel (I_1) + with a set of frontier vertices $\{v_4, v_5, v_2\}$



The BFS at v_4 terminates because the incoming edge label I_2 is an invalid state for kernel (I_1) +

The BFS at v_5 terminates because the only incoming neighbour with label $l_1^{},$ i.e., $v_2^{},$ has already been visited

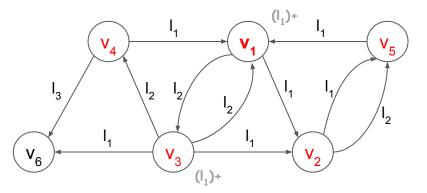
V	Lout(v)	Lin(v)
V ₁	$(v_1, l_2), (v_1, l_1)$	
V ₂	$(v_1^{}, l_1^{}), (v_1^{}, (l_2^{}, l_1^{}))$	
V ₃	$(v_1, l_2), (v_1, (l_2, l_1)), (v_1, l_2)$	
V ₄	(v ₁ , l ₁)	
V ₅	(v ₁ , l ₁)	
V ₆		

Backward KBS from v_1

Kernel search

→ Kernel BFS

The first kernel (I_1) + with a set of frontier vertices $\{v_4, v_5, v_2\}$



The BFS at v_1 terminates because incoming neighbours with $l_1,$ i.e., v_4 and $v_5, \ have already been visited$

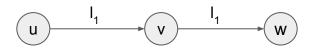
The BFS at v_{3} terminates because there is not incoming edges with label \mathbf{I}_{1}

V Lout(v) Lin(v) $(V_1, I_2), (V_1, I_1)$ **V**₁ $(V_1, I_1), (V_1, (I_2, I_1))$ V_2 $(v_1, l_2), (v_1, (l_2, l_1)), (v_1, l_1)$ V₃ (V_1, I_1) V_4 (V_1, I_1) V_5 V_6

The kernel BFS with (I_1) + terminates

Accessing order

- Accessing order:
 - The order of vertices, in which the indexing algorithm is performed
- Intuition:
 - Starting from the "middle"
- Example:
 - \circ \quad Less index entries with the order (v, u, w)
- Strategy:
 - \circ Sorting in (out-degree(v) + 1) x (in-degree(v) + 1)



The case with	Vertex v	Lout(v)	Lin(v)
(u, w, v)	u	(w, I ₁)	Ø
	V	(w, I ₁)	(u, l ₁)
	w	Ø	(u, l ₁)
The case with	Vertex v	Lout(v)	Lin(v)
(v, u, w)	u	(v, I ₁)	Ø
	V	Ø	Ø

w

Ø

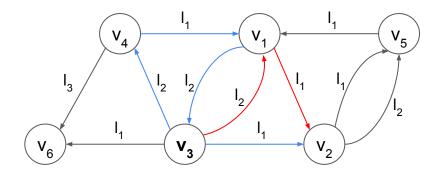
 (V, I_1)

- Efficient indexing:
 - When to skip index entries
 - When to terminate the KBS from a vertex early
- Intuition:
 - Concatenating edge-label sequences of sub-paths as much as possible
- Three pruning rules for efficient indexing
 - PR1 and PR2: skipping redundant entries
 - PR3: terminating the search early

Pruning Rules

- **PR1:** If the k-MR of an index entry that needs to be recorded can be acquired from the current snapshot of the RLC index, then the index entry can be skipped.
- **PR2:** If vertex v_i is visited by the backward KBS performed from vertex $v_{i'}$ s.t. $aid(v_{i'}) > aid(v_i)$, then the corresponding index entry can be skipped.
- **PR3:** If vertex v_i is visited by the kernel-BFS phase of a backward KBS performed from vertex $v_{i'}$, and PR1 (or PR2) is triggered, then vertex v_i and all the vertices in $in(v_i)$ are skipped.

PR1: If the k-MR of an index entry that needs to be recorded can be acquired from the current snapshot of the RLC index, then the index entry can be skipped.



The forward KBS from v_3 can visit v_2 , such that it tries to creat $(v_3, (l_2, l_1))$ in Lin (v_2)

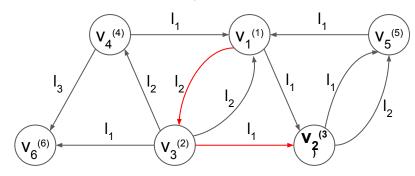
However, there already exists $(v_1, (l_2, l_1))$ in both Lout (v_3) and Lin (v_2) , such that the index entry that needs to be inserted can be pruned

The snapshot of the RLC index after performing KBS from $\rm v_1$

V	Lout(v)	Lin(v)
V ₁	$(v_1, l_2), (v_1, l_1), (v_1, (l_2, l_1))$	Ø
V ₂	(v ₁ , l ₁), (v ₁ , (l ₂ , l ₁))	$(v_1, l_1), (v_1, (l_2, l_1))$
V ₃	$(v_1, l_2), (v_1, (l_2, l_1)), (v_1, l_1)$	(v ₁ , l ₂), (v ₁ , (l ₁ , l ₂))
V ₄	(v ₁ , l ₁)	(v ₁ , l ₂)
v ₅	(v ₁ , l ₁)	(v ₁ , (l ₁ , l ₂)), (v ₁ , l ₁),
V ₆		(v ₁ , (l ₂ , l ₁))

PR2: If vertex v_i is visited by the backward KBS performed from vertex $v_{i'}$ s.t. $aid(v_{i'}) > aid(v_i)$, then the corresponding index entry can be skipped.

aid: accessing ID, e.g., $aid(v_3) = 2$



The backward KBS from v_2 can visit v_1 , such that it tries to creat $(v_2, (l_2, l_1))$ in Lout (v_1)

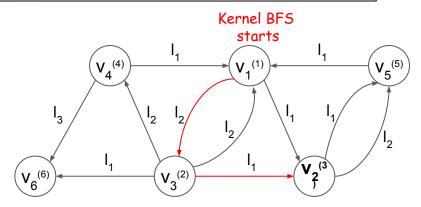
However, $aid(v_2) > aid(v_1)$, such that the index entry that needs to be inserted can be pruned

The snapshot of the RLC index after performing KBS from $v^{}_1$ and $v^{}_3$

V	Lout(v)	Lin(v)
V ₁	$(v_1, l_2), (v_1, l_1), (v_1, (l_2, l_1))$	Ø
V ₂	$(v_1^{}, l_1^{}), (v_1^{}, (l_2^{}, l_1^{}))$	$(v_1, l_1), (v_1, (l_2, l_1))$
V ₃	$(v_1, l_2), (v_1, (l_2, l_1)), (v_1, l_1), (v_3, (l_1, l_2))$	(v ₁ , l ₂), (v ₁ , (l ₁ , l ₂))
V ₄	$(v_1^{}, l_1^{}), (v_3^{}, (l_1^{}, l_2^{}))$	(v ₁ , l ₂)
V ₅	(v ₁ , l ₁), (v ₃ , (l ₁ , l ₂))	$(v_1, (l_1, l_2)), (v_1, l_1), (v_3, (l_1, l_2))$
V ₆	Ø	$(v_1, (l_2, l_1)), (v_3, l_1), (v_3, (l_2, l_3))$

28

PR3: If vertex v_i is visited by the kernel-BFS phase of a backward KBS performed from vertex $v_{i'}$, and PR1 (or PR2) is triggered, then vertex v_i and all the vertices in $in(v_i)$ are skipped.

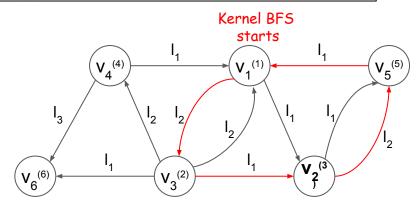


The backward KBS from v_2 is transformed from kernel search to kernel BFS guided by $(I_2, I_1)^+$ after visiting v_1

The snapshot of the RLC index after performing KBS from $v^{}_1$ and $v^{}_3$

V	Lout(v)	Lin(v)
V ₁	$(v_1, l_2), (v_1, l_1), (v_1, (l_2, l_1))$	Ø
V ₂	$(v_1^{}, l_1^{}), (v_1^{}, (l_2^{}, l_1^{}))$	$(v_1, l_1), (v_1, (l_2, l_1))$
V ₃	$(v_1, l_2), (v_1, (l_2, l_1)), (v_1, l_1), (v_3, (l_1, l_2))$	(v ₁ , l ₂), (v ₁ , (l ₁ , l ₂))
V ₄	(v ₁ , l ₁), (v ₃ , (l ₁ , l ₂))	(v ₁ , l ₂)
V ₅	(v ₁ , l ₁), (v ₃ , (l ₁ , l ₂))	$(v_1, (l_1, l_2)), (v_1, l_1), (v_3, (l_1, l_2))$
V ₆	Ø	$(v_1, (l_2, l_1)), (v_3, l_1), (v_3, (l_2, l_3))$

PR3: If vertex v_i is visited by the kernel-BFS phase of a backward KBS performed from vertex $v_{i'}$, and PR1 (or PR2) is triggered, then vertex v_i and all the vertices in $in(v_i)$ are skipped.



The backward KBS from v_2 is transformed from kernel search to kernel BFS guided by $(I_2, I_1)^*$ after visiting v_1

When the kernel BFS visits v_2 , it tries to creat $(v_2, (l_2, l_1))$ in Lout (v_2)

However, there exists $(v_1, (l_2, l_1))$ in both Lout (v_2) and Lin (v_2) , i.e., PR1 can be triggered

Then, the kernel BFS can terminate

The snapshot of the RLC index after performing KBS from v_1 and v_3

V	Lout(v)	Lin(v)
V ₁	$(v_1, l_2), (v_1, l_1), (v_1, (l_2, l_1))$	Ø
V ₂	$(v_1, l_1), (v_1, (l_2, l_1))$	$(v_1, l_1), (v_1, (l_2, l_1))$
V ₃	$\begin{array}{c} (v_1^{}, l_2^{}), (v_1^{}, (l_2^{}, \\ l_1^{})), (v_1^{}, l_1^{}), (v_3^{}, \\ (l_1^{}, l_2^{})) \end{array}$	(v ₁ , l ₂), (v ₁ , (l ₁ , l ₂))
V ₄	(v ₁ , l ₁), (v ₃ , (l ₁ , l ₂))	(v ₁ , l ₂)
V ₅	(v ₁ , l ₁), (v ₃ , (l ₁ , l ₂))	$(v_1, (l_1, l_2)), (v_1, l_1), (v_3, (l_1, l_2))$
V ₆	Ø	$(v_1, (l_2, l_1)), (v_3, l_1), (v_3, (l_2, l_3))$

30

Foundations of the indexing algorithm

No redundant index entries

Theorem 2: With pruning rules, the RLC index is condensed.

Correct and complete index

Theorem 3: Given an edge-labeled graph G and the RLC index of G with a positive integer k built by Algorithm 2, there exists a path from vertex s to vertex t in G which satisfies a label constraint L^+ , $|L| \le k$, if and only if one of the following condition is satisfied

(1)
$$\exists (x,L) \in \mathcal{L}_{out}(s)$$
 and $\exists (x,L) \in \mathcal{L}_{in}(t)$;
(2) $\exists (t,L) \in \mathcal{L}_{out}(s)$, or $\exists (s,L) \in \mathcal{L}_{in}(t)$.

The proofs are included in the full version on arXiv

Experimental setup

- Baselines
 - ETC (extended transitive closure): for every pairs of vertices, recording all the k-MRs
 - Online traversal: BFS and Bidirectional BFS
- 13 highly dense real-world graphs
- Workloads
 - 1000 true-queries and 1000 false-queries
- Parameter k
 - We start with k = 2, which is the real-world case
 - \circ Then, we analyze the cases of k = 2, 3, 4
- Implementation: Java 11
- Setting
 - 8 VCPUs of 2.4GHz; 128GB main memory
 - Heap size of JVM: 120GB

IABLE III							
OVERVIEW OF REAL-WORLD GRAPHS.							
V	E	$ \mathbb{L} $	Synthetic Labels	Loop Count	Triangle Count		
6K	51K	3		4K	98K		
75K	508K	8	\checkmark	0	1.6M		
465K	834K	8	\checkmark	0	38K		
325K	1.4M	8	\checkmark	27K	8.9M		
281K	2M	8	\checkmark	0	11 M		
875K	5M	8	\checkmark	0	13M		
2.3M	5M	8	\checkmark	0	9M		
685K	7M	8	\checkmark	0	64M		
1.7M	28.5M	8	\checkmark	4K	52M		
1.6M	30.6M	8	\checkmark	0	32M		
2.6M	63.4M	3		15M	114M		
4.8M	68.9M	50	\checkmark	0	285M		
3.3M	123.7M	25	\checkmark	19K	30B		
	V 6K 75K 465K 325K 281K 875K 2.3M 685K 1.7M 1.6M 2.6M 4.8M	Image: RVIEW OF REAL- IV E 6K 51K 75K 508K 465K 834K 325K 1.4M 281K 2M 875K 5M 2.3M 5M 685K 7M 1.7M 28.5M 1.6M 30.6M 2.6M 63.4M 4.8M 68.9M	Image: Relation of the second system of	Image: Non-additional system of the image: Non-additity of the image: Non-additional system of the image:	RVIEW OF REAL-WORLD GRAPHS. $ V $ $ E $ $ \mathbb{L} $ Synthetic Labels Loop Count 6K 51K 3 4K 75K 508K 8 \checkmark 0 465K 834K 8 \checkmark 0 325K 1.4M 8 \checkmark 0 875K 5M 8 \checkmark 0 1.3M 5M 8 \checkmark 0 685K 7M 8 \checkmark 0 1.7M 28.5M 8 \checkmark 0 1.6M 30.6M 8 \checkmark 0 2.6M 63.4M 3 15M 4.8M 68.9M 50 \checkmark 0		

TABLE III

g-rpqs/ rlc-index							
This repository provides the RLC index, a reachability index for processing graph queries with a concatenation of edge labels under							
유 1 Contributor	⊙ 0 Issues	☆ 3 양 0 Stars Fork	(S				

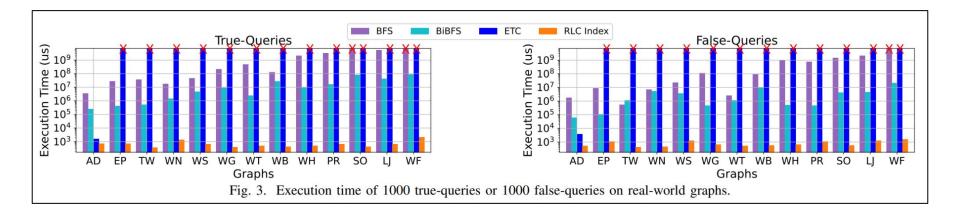
https://github.com/g-rpqs/rlc-index

Indexing performance

- Building ETC is not feasible
 - Building ETC timed out in 24 hours or run out of memory except for the AD graph
- Four-orders-of-magnitude improvement
- Effectiveness of the pruning rules
 - Although SO requires more indexing time than LJ and WF, index size of the former is smaller than those of the latter

TABLE IV							
INDEXING TIME (IT) and index size (IS) .							
Dataset	RLC	Index	ETC				
	IT (s)	IS (MB)	IT (s)	IS (MB)			
AD	0.7	1.9	2216.1	2798.7			
EP	22.6	29.3	-	-			
TW	8.1	93.5	-	-			
WN	33.1	122.6	-	-			
WS	53.5	173.9	-	-			
WG	101.3	403.6	-	-			
WT	812.9	607.1	-	-			
WB	167.1	474.2	-	-			
WH	3707.2	1319.1	-	-			
PR	3104.1	1212.6	-	-			
SO	57072.5	844.2	-	-			
LJ	18240.9	6248.1	-	-			
WF	51338.7	6467.9	-	-			

Query performance

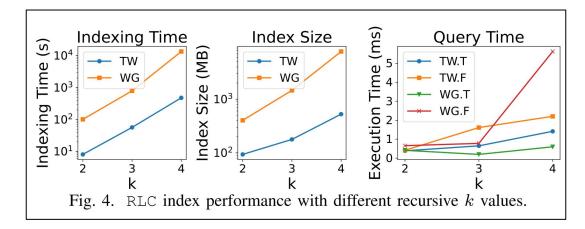


Executing 1000 queries using the RLC index takes 1 ms except the WF graph that is 2 ms

Up to six-orders-of-magnitude improvement over BFS

Up to four-orders-of-magnitude improvement over bidirectional BFS

Impact of k

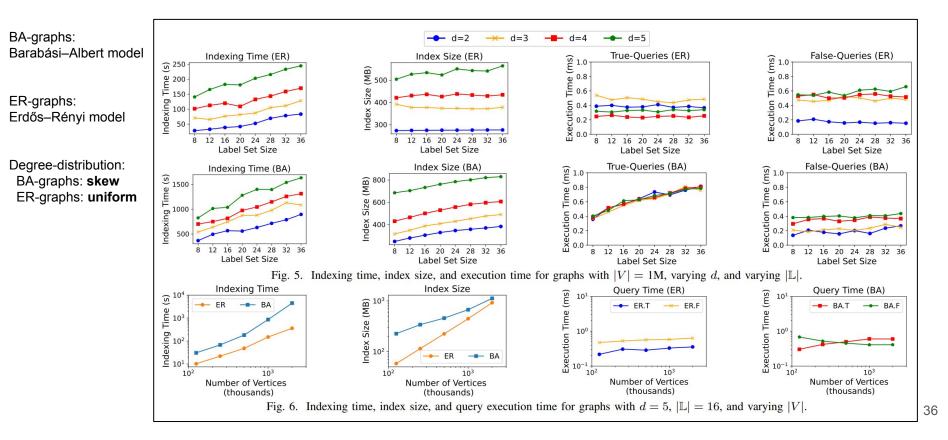


Both indexing time and index size will increase when the value k increases

Query time also increases a bit due to the large index size

The number of path-constraints or kernels will exponentially grow as the increase of k

Impact of graph characteristics



Comparison with existing systems

• Systems

- Commercial and open-sourced systems
- Virtuoso (v7.2.6.3233), Sys1, and Sys2
- Dataset: the WN graph
 - |V|: 325K
 - |E|: 1.4M
- RLC index built with k = 3
 - 5.9 minutes
 - 821 megabytes

TABLE VSpeed-ups (SU) and workload size break-even points (BEP) of The RLC index over graph engines.								
	RLC Query					Extended Query		
Sys.	Q1		Q2		Q3		Q4	
	SU	BEP	SU	BEP	SU	BEP	SU	BEP
Sys1	1200x	84100	10400x	34000	18400x	9400	34000x	300
Sys2	3000x	34900	202000x	1700	1300000x	130	104000x	98
Virtuoso	597x	180000	4900x	71700	38100000x	5	3-1	-

BEP indicates when the RLC index should be built

The RLC index built for Q3 can also significantly improve the execution time of Q1, Q2, and Q4 as well

Conclusion

- RLC queries
 - Reachability queries with a path constraint based on the Kleene plus over a concatenation of edge labels
- RLC index
 - Evaluating RLC queries through path concatenation
- Indexing algorithm
 - Backward and forward kernel-based search with pruning rules
- Experimental evaluation
 - RLC index can significantly improve query processing while reduce offline indexing overhead

Thank you and Q&A